

**In the Specification:**

Please insert before the first paragraph of the application:

This application is the national stage application of international application number PCT/DE03/03131, filed on September 19, 2003, which claims the benefit of priority to German Patent Application 102 45 153.2, filed on September 27, 2002, incorporated herein by reference.

Please delete the paragraph on page 1, lines 24-27.

Please rewrite the paragraph on page 11, lines 12-19 as follows:

Regions comprising monocrystalline silicon are solid white in the figures, see for example the substrate region 14 in Figure 3. Polycrystalline regions are covered with crosses, see for example the gate regions 20 and 22 and also the polyregions 24 and 26 in Figure 3. Regions comprising metal are hatched vertically, see for example the metal interconnect 50, which comprises copper or aluminum, for example.